

2722



Docket No.: 50246-068

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Robert M. NALLY, et al.

Serial No.: 09/374,041

Group Art Unit: 2722

Filed: August 13, 1999

Examiner: M. Powell

For: APPARATUS, SYSTEMS AND METHODS FOR CONTROLLING GRAPHICS AND  
VIDEO DATA IN MULTIMEDIA DATA PROCESSING AND DISPLAY SYSTEMS

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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INFORMATION DISCLOSURE  
CITATION IN AN  
APPLICATION

(PTO-1449)



ATTY. DOCKET NO.  
**50246-068**

SERIAL NO.  
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APPLICANT  
**Robert M. NALLY, et al.**

FILING DATE  
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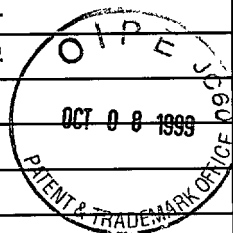
GROUP  
**2722**

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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,559,954	9/24/96	Sakoda, et al.			
	5,640,332	6/17/97	Baker, et al.			
	5,097,257	3/17/92	Clough, et al.			
	5,546,531	8/13/96	Lippincott			
	5,335,321	8/2/94	Harney, et al.			
	4,599,611	7/8/86	Bowker, et al.			
	3,618,035	4/17/69	Simms, et al.			
	3,673,324	6/27/72	Ito, et al.			
	4,498,098	2/5/85	Stell			
	4,425,581	1/10/84	Schweppe, et al.			
	5,420,643	5/30/95	Romesburg, et al.			
	5,752,010	5/12/98	Herbert			
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	5,291,188	3/1/94	McIntyre, et al.			
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	5,640,332	6/17/97	Baker, et al.			
	5,097,257	3/17/92	Clough, et al.			
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	5,396,263	3/7/95	Seiler, et al.			



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"Intel's i750 ® Video Processor—The Programmable Solution," Manepally, et al. Intel

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	"ET4000/W32 Graphics Accelerator Data Book, Tseng Labs, ATI002488-ATI002744.
	"Tseng LabsET4000/W32I Graphics Accelerator," May 12, 1993, ATI017493-498.
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	"PxVPS Programmer's Reference Manual," October 1993, Pixel Semiconductor, CL049172-049363.
	"User Interface," Guide (ATI020390-94)
EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.



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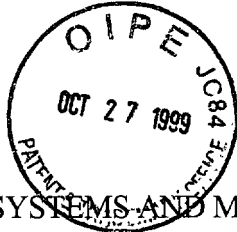
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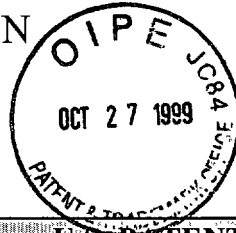
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## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

## FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER

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